


PATENT APPLICATION

042390.P9741

Amendment

Amendment to Claims

Please amend the claims as shown below.

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1. (Currently Amended) A mobile communication device [An apparatus] comprising:
- a first processor adapted to execute a user application;
 - a second processor adapted to process a wireless communication, wherein the second processor is capable of initiating the wireless communication independently of the first processor; and
 - an input port coupled to the first processor and the second processor.
2. (Currently amended) The mobile communication device [apparatus] of claim 1, further comprising a display, wherein the first processor and the second processor are further adapted to display information on the display.
3. (Currently amended) The mobile communication device [apparatus] of claim 1, further comprising an interface adapted to couple the first processor to the second processor.
4. (Currently amended) The mobile communication device [apparatus] of claim 3, wherein the interface comprises a Peripheral Components bus.
5. (Currently amended) The mobile communication device [apparatus] of claim 3, wherein the interface comprises a serial bus.

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6. (Currently amended) The mobile communication device [apparatus] of claim 3, wherein the interface is adapted to provide the second processor user data from the input port.

7. (Currently amended) The mobile communication device [apparatus] of claim 1, further comprising:

- a first memory coupled to the first processor; and
- a second memory coupled to the second processor.

8. (Currently amended) The mobile communication device [apparatus] of claim 1, further comprising:

- a first power source coupled to the first processor; and
- a second power source coupled to the second processor.

9. (Currently amended) The mobile communication device [apparatus] of claim 1, wherein the second processor comprises a digital signal processor.

10. (Currently amended) The mobile communication device [apparatus] of claim 1, wherein the first processor is further adapted to execute a user application independently of the second processor.

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11. (Currently amended) A mobile communication device [system] comprising:

- a non-volatile memory;
- an input port;
- an application subsystem coupled to the input port; and
- a wireless subsystem coupled to the input port and to the non-volatile memory.

12. (Currently amended) The mobile communication device [system] of claim 11, further comprising an interface to couple the application subsystem to the wireless subsystem.

13. (Currently amended) The mobile communication device [system] of claim 12, wherein the interface comprises a serial interface.

14. (Currently amended) The mobile communication device [system] of claim 11, wherein the wireless subsystem comprises a digital signal processor.

15. (Currently amended) The mobile communication device [system] of claim 14, wherein the wireless subsystem further comprises a transmitter and a receiver.

16. (Currently amended) The mobile communication device [system] of claim 11, wherein the application subsystem is adapted to execute a user application and process data provided with the input port.

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17. (Currently amended) The mobile communication device [system] of claim 12, wherein the interface couples the wireless subsystem to the input port.

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18. (Currently amended) A method of processing a communication comprising:

providing data to an application subsystem through an input port; and

providing data to a wireless subsystem through the input port to initiate a wireless communication, the wireless subsystem and the application subsystem being within a mobile communication device.

19. (Original) The method of claim 18, wherein providing data to the application subsystem includes providing data through an interface.

20. (Original) The method of claim 18, wherein providing data to the wireless subsystem includes providing data through an interface.

21. (Original) The method of claim 19, further comprising executing an application with the application subsystem independently of the wireless subsystem.
